

REMARKS

Applicants appreciate the Examiner's allowance of Claims 30-39.

Applicants will now address each of the Examiner's remaining objections and rejections in the order in which they appear in the Office Action.

Drawing

In the Office Action, the Examiner is requesting that Figs. 1-15 be labeled as "Prior Art." Applicants believe that this is a typographical error and that only Figs. 13-15 should be labeled as "Prior Art." Such an interpretation is supported by the Brief Description of the Drawings and the specification as filed.

Accordingly, Applicants have amended Figs. 13-15 to add the label "Prior Art." Therefore, it is respectfully requested that this objection be withdrawn.

Claim Rejections - 35 USC §102

In the Office Action, the Examiner rejects Claims 1, 2, 23 and 27 under 35 USC §102(e) as being anticipated by Mizutani et al. This rejection is respectfully traversed.

More specifically, independent Claims 1 and 2 require a plurality of pairs of memory circuits. The Examiner contends that reference numerals 105 and 106 in Fig. 14 in Mizutani disclose this claimed feature. However, col. 9, lns. 50-53 of Mizutani recite "a buffer 105 for compensating a transfer voltage level and a buffer 106 for compensating an inversion [sic] current due to spontaneous polarization of the liquid crystal 2" [emphasis added]. Hence, Mizutani does not disclose or suggest the claimed invention. Therefore, the claims are patentable thereover, and it is respectfully requested that this rejection be withdrawn.

Claim Rejections - 35 USC §103

The Examiner also rejects Claims 2, 4, 6-9 and 23-29 under 35 USC §103(a) as being unpatentable over Mizutani and rejects Claims 3 and 5-6 under 35 USC §103(a) as being unpatentable over Mizutani further in view of Kanno. These rejections are also respectfully traversed.

Independent Claims 2, 4, 7, 8 and 9 also require a plurality of pairs of memory circuits. The Examiner contends that reference numerals 105 and 106 in Fig. 14 in Mizutani disclose this claimed feature. As explained above, col. 9, lns. 50-53 of Mizutani recite “a buffer 105 for compensating a transfer voltage level and a buffer 106 for compensating an inversion [sic] current due to spontaneous polarization of the liquid crystal 2” [emphasis added]. Hence, Mizutani does not disclose or suggest the claimed invention. Therefore, the claims are patentable thereover, and it is respectfully requested that this rejection be withdrawn.

Conclusion

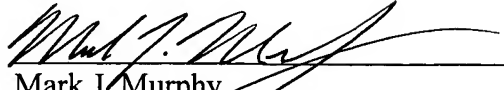
Accordingly, it is respectfully submitted that the present application is in a condition for allowance and should be allowed.

If any further fee is due for this amendment, please charge our Deposit Account No. 50-1039.

Favorable reconsideration is earnestly solicited.

Respectfully submitted,

Date: *November 24, 2004*


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IN THE DRAWINGS:

The attached sheets of drawings include changes to Figs. 13-15. In particular, each of these figures has been labeled "Prior Art"

Attachment: Replacement Sheets

Annotated Sheets showing changes

Marked-up
copy



FIG. 13

PRIOR ART

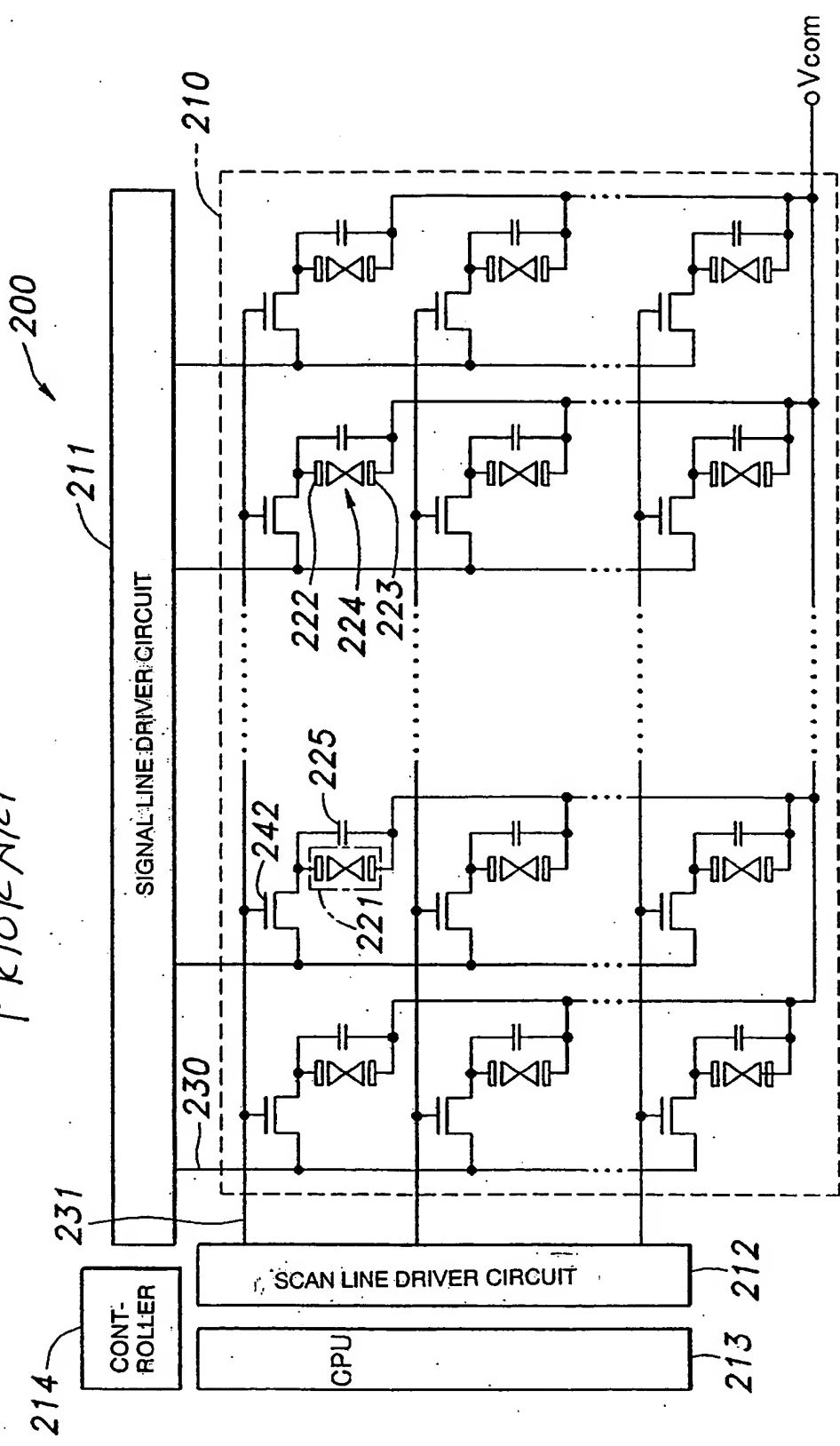


FIG. 14 PRIOR ART

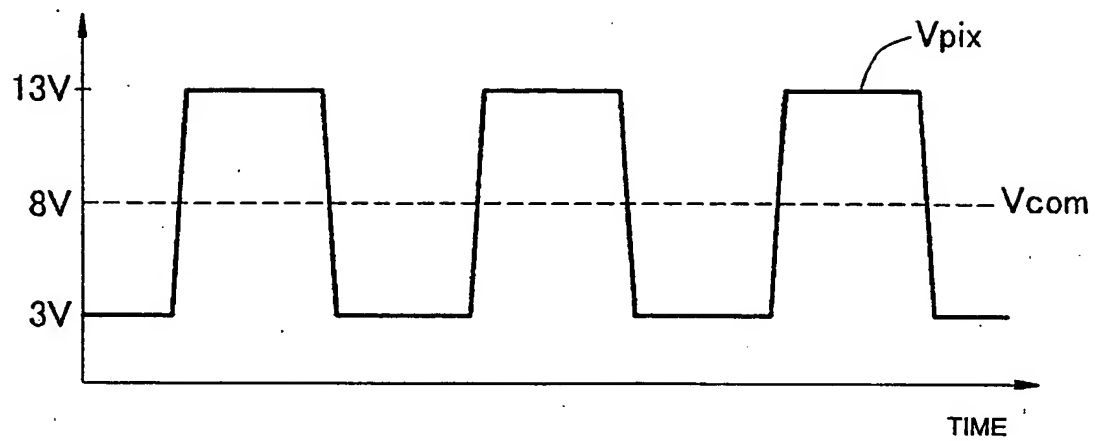


FIG. 15 PRIOR ART

